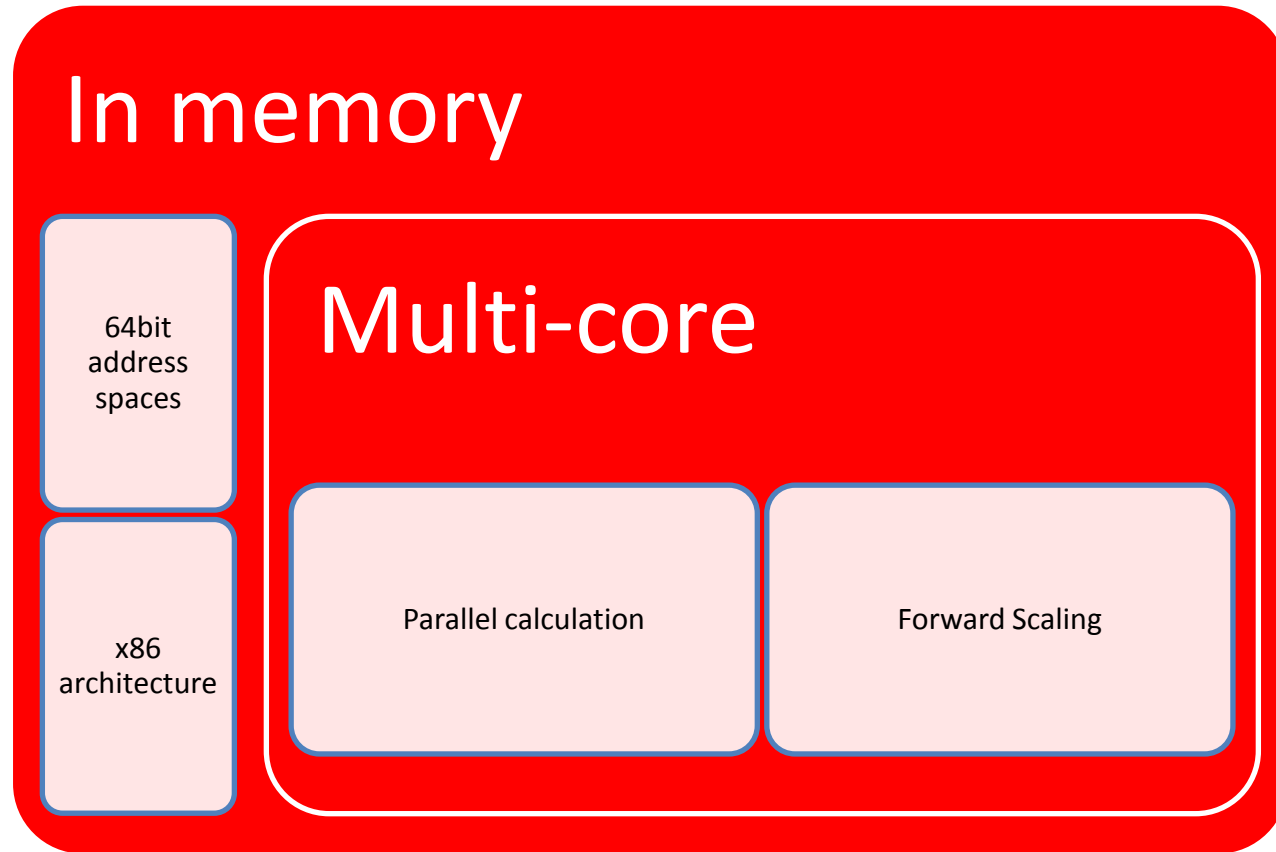
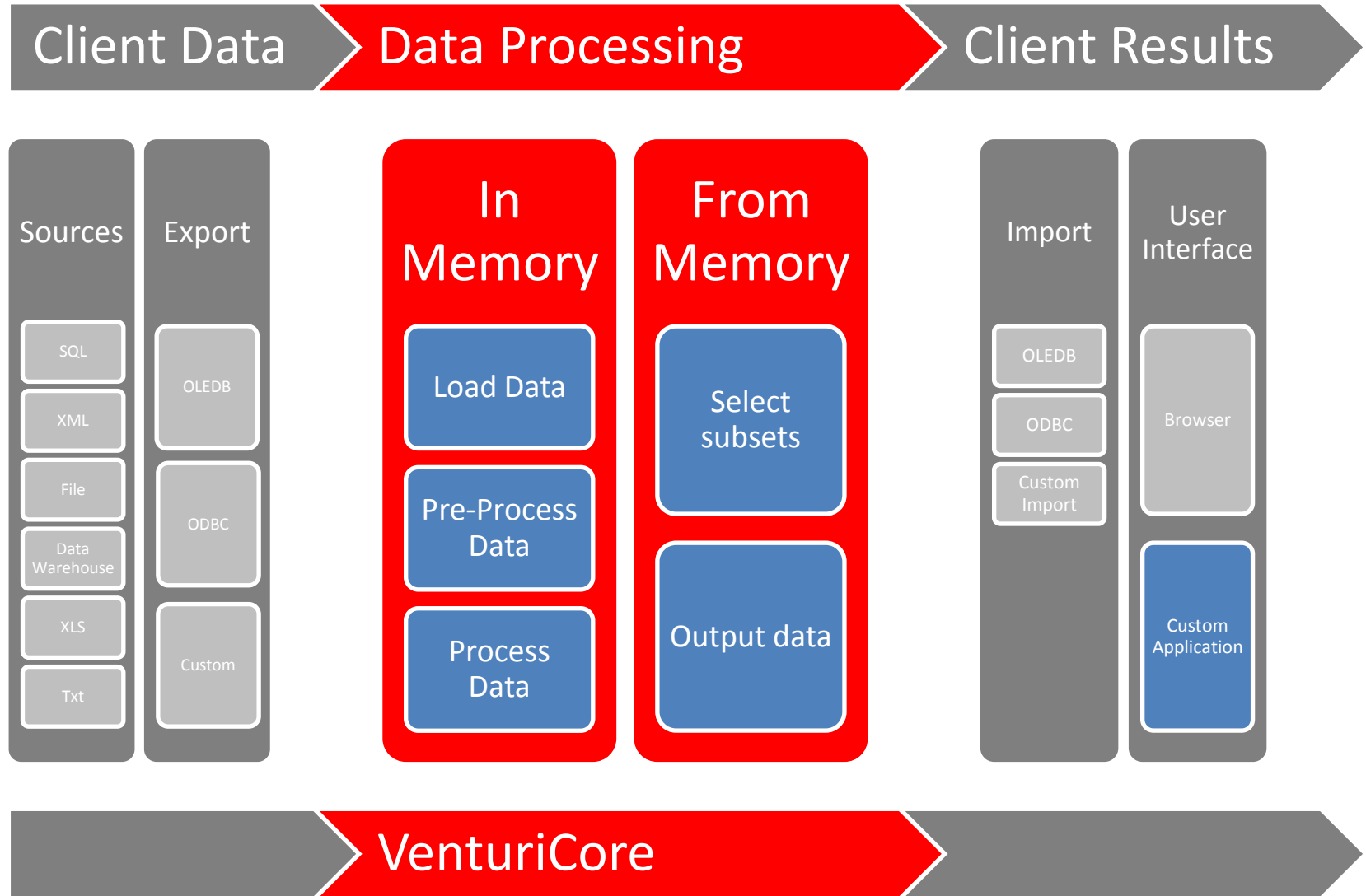


What VenturiCore is...



Where VenturiCore fits...



How VenturiCore works...

DAG Based

- Allows minimum recalculation
- Generationally scheduled

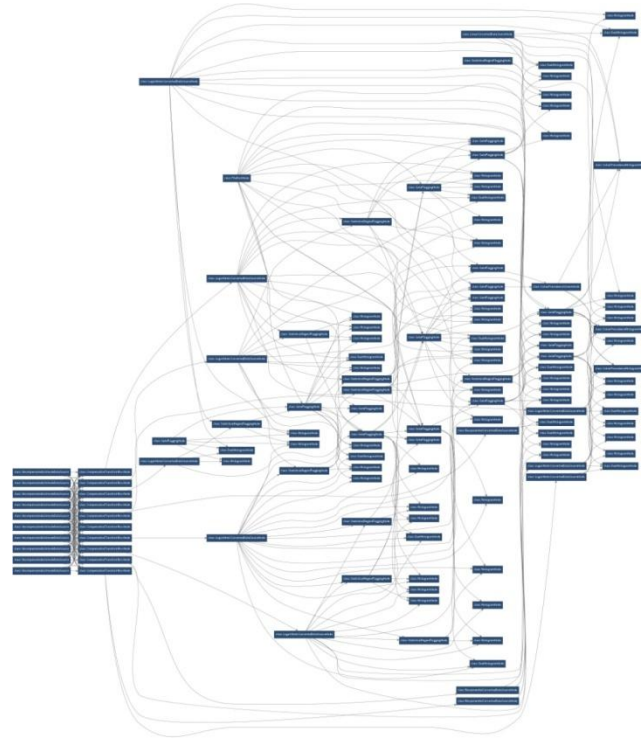
Automated multi-threading

- Less expensive to implement
- Goes faster with more processors

Processor Cache Management

- Accelerates performance
- Allows optimisation of heterogeneous systems

How VenturiCore works in detail...

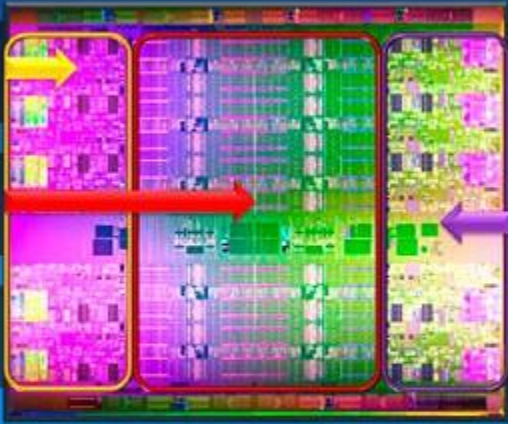


Scheduling and executing tasks with generational efficiency.

United States Patent Application 20090007127

Licensed by Core, by Application and by Enterprise

Introducing the Intel® Xeon® Processor E7 Family



Up to 10 Cores and 20 Threads

30MB of Last Level Cache

Advanced Encryption Standard – New Instructions

Up to 2 Terabytes of DDR3 Memory¹ and Low Voltage DIMM Support

Intel® Trusted Execution Technology

Accelerating Mission Critical Transformation

1 On 45 system using 32 GB DIMMs

intel Xeon intel



Testing VenturiCore with Intel

E7 processors promise greater efficiency in data processing.

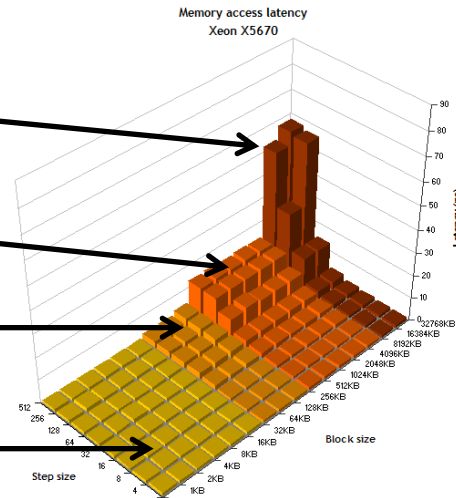
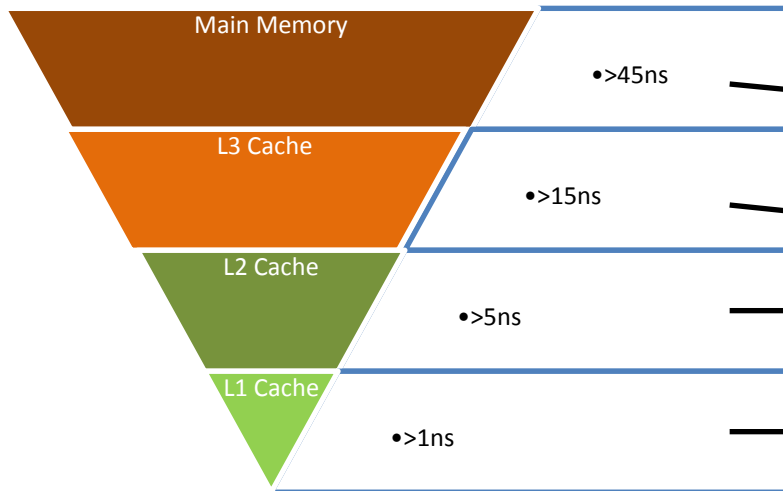
Testing platforms are Xeon 5670, 6 cores and E7, 10 cores.

August/September 2011

Maintaining throughput on UCS Xeon x5670 and E7 systems:

VenturiCore actively manages the data flow through each processor package.

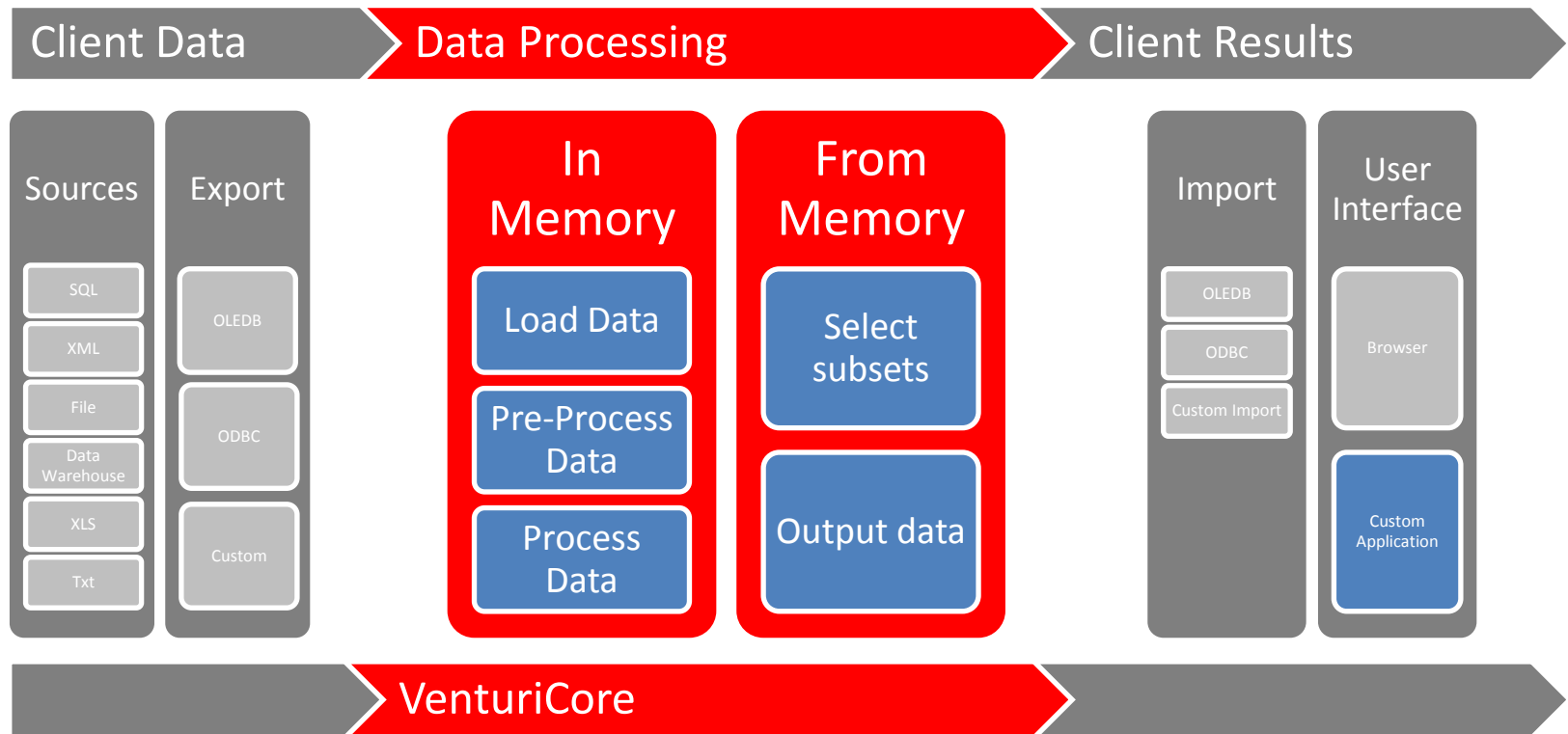
Memory access times



Intel & AC confirmed that VenturiCore uses L1 & L2 Cache more than 99% of the time during data processing using E7 systems.

This factor alone would account for a 10 fold speed increase in data processing using VenturiCore; this technology makes extensive use of the pre-fetch capabilities of the Intel Nehalem processor family.

Use cases...



Big Data = Big Memory

Option calculations; VaR models;

Co-location; JVM extender

Contact

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